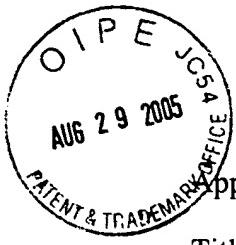


JFw

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865 Filed: December 5, 2003

Examiner: Nguyen, Van Thu T. Group Art Unit: 2824

Atty. Docket No.: 023-0029 Confirmation No.: 8494

August 24, 2005

Mail Stop Amendment
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37 C.F.R. § 1.97(b)

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings to the Examiner's attention in the above-identified application the patents, publications, applications or other information identified in the attached:

- Form(s) PTO/SB/08A (1 page(s), with copy of 2 reference(s)).
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Citation of such information shall not be construed as (i) an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention, (ii) a representation that a search has been made, other than as described herein, or (iii) an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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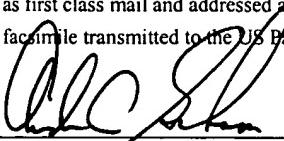
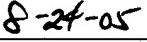
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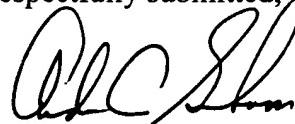
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Respectfully submitted,



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PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: 023-0029
	Application No.: 10/729,865
O I P E INFORMATION DISCLOSURE STATEMENT BY APPLICANT AUG 29 2005 SEARCHED & INDEXED SHEET 11 OF 14 MAILED	First Named Inventor: En-Hsing Chen Filing Date: December 5, 2003 Group Art Unit: 2824 Examiner Name: Nguyen, Van Thu T. Date Submitted: August 24, 2005

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	AA	Patent Abstracts of Japan, Vol. 1998, No. 11	09-30-1998	Arase Kenshirou		
	AB	JP-1998-10149688	06-02-1998	Arase Kenshirou		
Examiner Signature				Date Considered		

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